#### CSci 5271 Introduction to Computer Security Low-level attacks and defenses (combined lecture)

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#### Outline

# Shellcode techniques

- Exploiting other vulnerabilities
- Announcements intermission
- Return address protections
- ASLR and counterattacks

W⊕X (DEP)

# Basic definition Shellcode: attacker supplied instructions implementing malicious functionality Name comes from example of starting a shell Often requires attention to machine-language encoding

# Classic execve /bin/sh

- execve(fname, argv, envp)
  system call
- Specialized syscall calling conventions
- Omit unneeded arguments
- Doable in under 25 bytes for Linux/x86







#### Multi-stage approach

- Initially executable portion unpacks rest from another format
- Improves efficiency in restricted environments
- But self-modifying code has pitfalls

![](_page_1_Figure_5.jpeg)

![](_page_1_Figure_6.jpeg)

![](_page_1_Figure_7.jpeg)

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#### Non-control data overwrite

Overwrite other security-sensitive data
 No change to program control flow
 Set user ID to 0, set permissions to all, etc.

![](_page_2_Figure_4.jpeg)

![](_page_2_Figure_5.jpeg)

![](_page_2_Figure_6.jpeg)

![](_page_3_Figure_0.jpeg)

![](_page_3_Figure_1.jpeg)

![](_page_3_Figure_2.jpeg)

![](_page_3_Figure_3.jpeg)

# Format string attack: overwrite

- %n specifier: store number of chars written so far to pointer arg
- Advance format arg pointer to other attacker-controlled data
- Control number of chars written with padding
- On x86, use unaligned stores to create pointer

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- **Return address protections**
- ASLR and counterattacks
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#### **Readings reminders**

- Lectures are a bit behind, but keeping on reading schedule is still a good idea
- Relevant for today: attack techniques under ASLR
- For academic (ACM) papers, use campus/proxy downloads

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W⊕X (DEP)

![](_page_4_Figure_7.jpeg)

![](_page_4_Figure_8.jpeg)

![](_page_5_Figure_0.jpeg)

- 🖲 Adjacent structure fields
- Adjacent static data objects

#### **<u>Linux/x86</u>**: %gs:0x14

![](_page_5_Figure_4.jpeg)

![](_page_5_Figure_5.jpeg)

#### Shadow return stack

- Suppose you have a safe place to store the canary
- Why not just store the return address there?
- Needs to be a separate stack
- Ultimate return address protection

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W⊕X (DEP)

#### Basic idea

 "Address Space Layout Randomization"
 Move memory areas around randomly so attackers can't predict addresses
 Keep internal structure unchanged

 E.g., whole stack moves together

## Code and data locations

- Execution of code depends on memory location
- **6** E.g., on 32-bit x86:
  - Direct jumps are relative
  - Function pointers are absolute
  - Data must be absolute

# Relocation (Windows)

- Extension of technique already used in compilation
- Keep table of absolute addresses, instructions on how to update
- Disadvantage: code modifications take time on load, prevent sharing

# PIC/PIE (GNU/Linux)

- "Position-Independent Code / Executable"
- Keep code unchanged, use register to point to data area
- Disadvantage: code complexity, register pressure hurt performance

![](_page_7_Figure_0.jpeg)

#### **Entropy limitations**

- Intuitively, entropy measures amount of randomness, in bits
- Random 32-bit int: 32 bits of entropy
- ASLR page aligned, so at most 32 12 = 20 bits of entropy
- Other constraints further reduce possibilities

![](_page_7_Figure_6.jpeg)

## GOT hijack (Müller)

printf@plt: jmp \*0x8049678

system@plt: jmp \*0x804967c

. . .

. . .

0x8049678: <addr of printf in libc> 0x804967c: <addr of system in libc>

![](_page_7_Figure_11.jpeg)

![](_page_8_Figure_0.jpeg)

![](_page_8_Figure_1.jpeg)

![](_page_8_Figure_2.jpeg)

![](_page_8_Figure_3.jpeg)

![](_page_8_Figure_4.jpeg)

![](_page_9_Figure_0.jpeg)

#### Counterattack: code reuse

- Attacker can't execute new code
- So, take advantage of instructions already in binary
- There are usually a lot of them
- And no need to obey original structure

![](_page_9_Figure_6.jpeg)

![](_page_9_Figure_7.jpeg)

# Beyond return-to-libc

- 🖲 Can we do more? Oh, yes.
- Classic academic approach: what's the most we could ask for?
- Here: "Turing completeness"
- How to do it: reading for Thursday