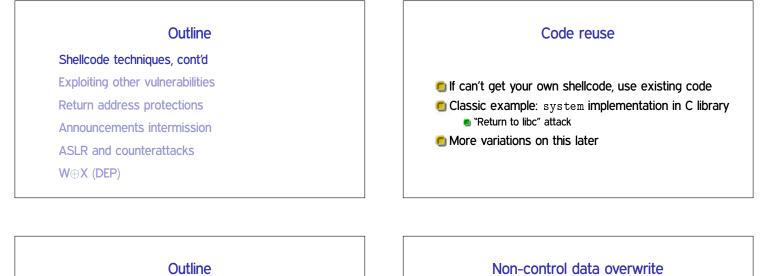
CSci 5271 Introduction to Computer Security Low-level attacks and defenses

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Preview question

What two methods are mentioned in the StackGuard paper to prevent canary forgery?

- A. "terminator canary" and "random canary"
- B. "StackGhost" and "random XOR canary"
- C. "stack layout randomization" and "entropy canary"
- D. "StackGhost" and "PointGuard"
- E. "Keccak" and "Rijndael"



Shellcode techniques, cont'd Exploiting other vulnerabilities **Return address protections** Announcements intermission ASLR and counterattacks W⊕X (DEP)



- No change to program control flow
- Set user ID to 0, set permissions to all, etc.

Unallocated

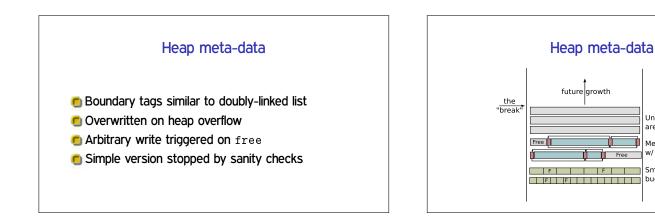
Medium objects w/ boundary tags

Small objects

bucketed by size

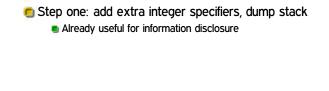
area

ľ

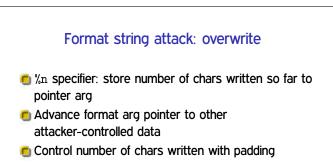




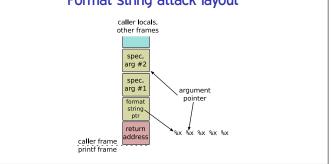
- Allocate data on the zero page
 - Most common in user-space to kernel attacks
 - Read more dangerous than a write





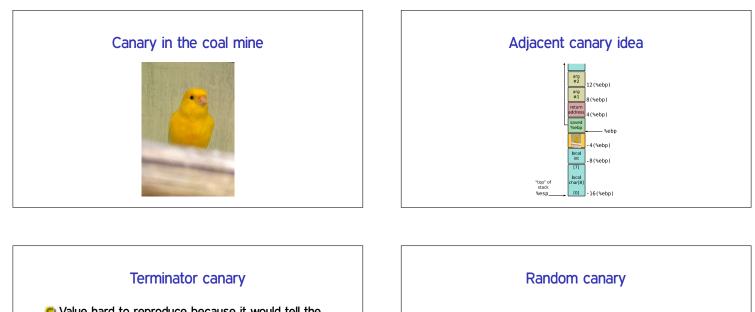


On x86, use unaligned stores to create pointer

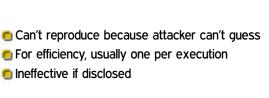


Outline

Shellcode techniques, cont'd Exploiting other vulnerabilities **Return address protections** Announcements intermission ASLR and counterattacks W \oplus X (DEP)



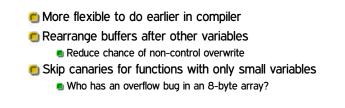
- Value hard to reproduce because it would tell the copy to stop
 StackGuard: 0x00 0D 0A FF
 - O: String functions
 newline: fgets(), etc.
 - **-1**: getc()
 - carriage return: similar to newline?
- 🖲 Doesn't stop: memcpy, custom loops



XOR canary

- Want to protect against non-sequential overwrites
- SOR return address with value c at entry
- **5** XOR again with c before return
- Standard choice for c: see random canary

Further refinements





Complex anti-canary attack

Canary not updated on fork in server
Attacker controls number of bytes overwritten

Complex anti-canary attack

- Canary not updated on fork in server
- Attacker controls number of bytes overwritten
- ANRY BNRY CNRY DNRY ENRY FNRY
- **O** search $2^{32} \rightarrow$ search $4 \cdot 2^8$

Shadow return stack

- Suppose you have a safe place to store the canary
- Why not just store the return address there?
- Needs to be a separate stack
- Ultimate return address protection

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Shellcode techniques, cont'd Exploiting other vulnerabilities Return address protections Announcements intermission ASLR and counterattacks $W \oplus X$ (DEP)

Integer overflow question

Which of the following is not always true, when the variables are interpreted as 32-bit unsigned ints in C?

A. x*y is odd, if both x and y are odd B. x*y == y*xC. x + x + x + x == 4*x

- D. 16*x >=x
- **E**. x + (-x) == 0

Pre-proposals due tonight

- Most groups formed?
- One PDF per group, include schedule choices
- 🖲 Submit via Canvas by 11:59pm

HA1 VMs now available Request from Travis if you have not already First exploit is due Friday evening Shouldn't be too hard to find, but allow time for trying out the VM and testing

BCECHO

- An even simpler buffer overflow example
- Can compile like BCMTA, install setuid root
- Will use for attack demo purposes next week

Outline

Shellcode techniques, cont'd Exploiting other vulnerabilities Return address protections

Announcements intermission

ASLR and counterattacks

W⊕X (DEP)

Basic idea "Address Space Layout Randomization" Move memory areas around randomly so attackers can't predict addresses Keep internal structure unchanged E.g., whole stack moves together

 Code and data locations
 Relocation (Windows)

 Execution of code depends on memory location
 Extension of technique already used in compilation

 E.g., on 32-bit x86:
 Keep table of absolute addresses, instructions on how to update

 Function pointers are absolute
 Disadvantage: code modifications take time on load, prevent sharing

PIC/PIE (GNU/Linux)

- "Position-Independent Code / Executable"
- Keep code unchanged, use register to point to data area
- Disadvantage: code complexity, register pressure hurt performance

What's not covered

- Main executable (Linux 32-bit PIC)
- Incompatible DLLs (Windows)
- Relative locations within a module/area

Entropy limitations

- Intuitively, entropy measures amount of randomness, in bits
- Random 32-bit int: 32 bits of entropy
- ASLR page aligned, so at most 32 12 = 20 bits of entropy
- Other constraints further reduce possibilities

Leakage limitations

- If an attacker learns the randomized base address, can reconstruct other locations
- **[** Any stack address \rightarrow stack unprotected, etc.

GOT hijack (Müller)

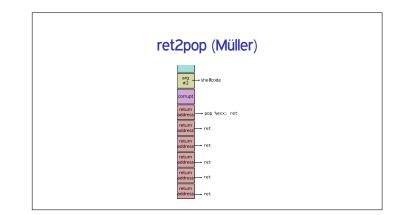
Main program fixed, libc randomized
 PLT in main program used to call libc
 Rewire PLT to call attacker's favorite libc functions
 E.g., turn printf into system

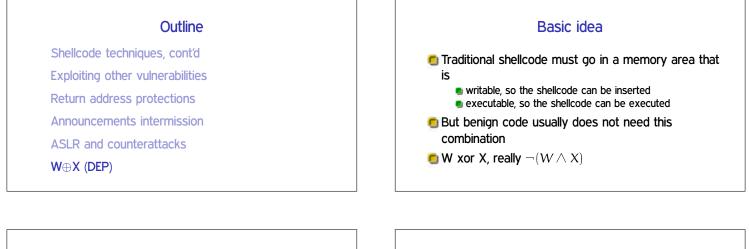
GOT hijack (Müller)

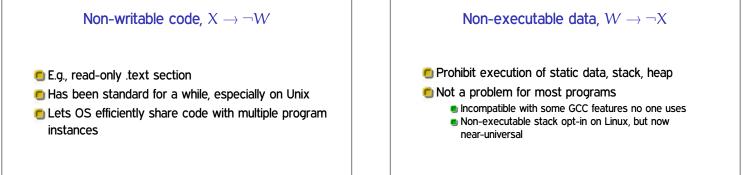
printf@plt:	jmp	*0x8049678	
system@plt:	jmp	*0x804967c	
		printf in libc> system in libc>	

ret2pop (Müller)

- Take advantage of shellcode pointer already present on stack
- Rewrite intervening stack to treat the shellcode pointer like a return address
 - A long sequence of chained returns, one pop







Implementing $W \oplus X$

One important exception

- Remaining important use of self-modifying code: just-in-time (JIT) compilers

 E.g., all modern JavaScript engines

 Allow code to re-enable execution per-block
 - Allow code to re-enable execution per-bio
 - mprotect, VirtualProtect
 Now a favorite target of attackers
- Counterattack: code reuse Attacker can't execute new code So, take advantage of instructions already in binary There are usually a lot of them And no need to obey original structure Classic return-to-libc (1997) Classic return-to-libc (1997) Classic return-to-libc (1997) Classic return-to-libc (1997) Classic return-to-libc (1997)

Chained return-to-libc

Shellcode often wants a sequence of actions, e.g.

Restore privileges

- Allow execution of memory area
- Overwrite system file, etc.
- Can put multiple fake frames on the stack
 - Basic idea present in 1997, further refinements

Beyond return-to-libc

- 🖲 Can we do more? Oh, yes.
- Classic academic approach: what's the most we could ask for?
- 🖲 Here: "Turing completeness"
- How to do it: reading for Monday

